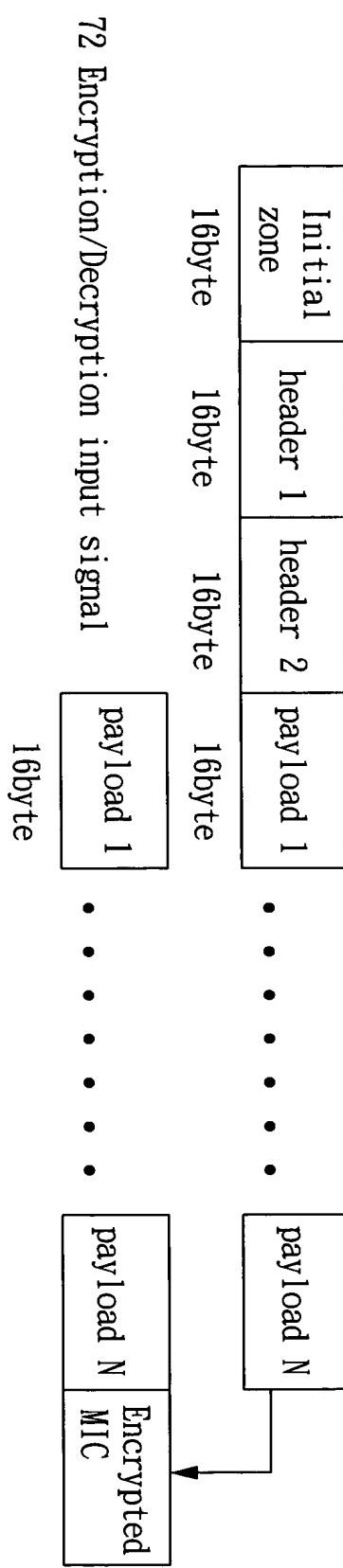


FIG. 1

70 Integrity input signal



72 Encryption/Decryption input signal

1
2
3
4

N+3

FIG. 2

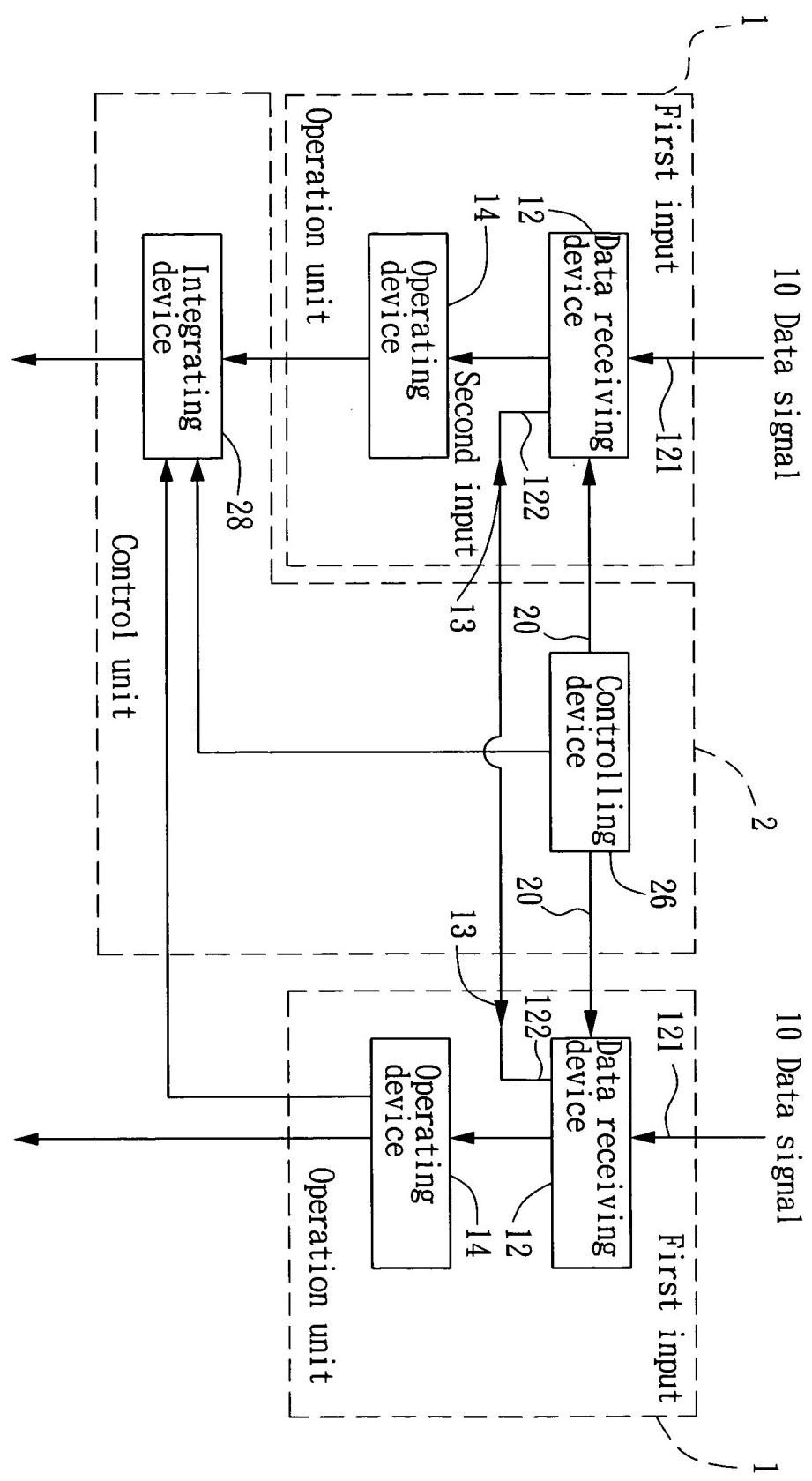


FIG. 3

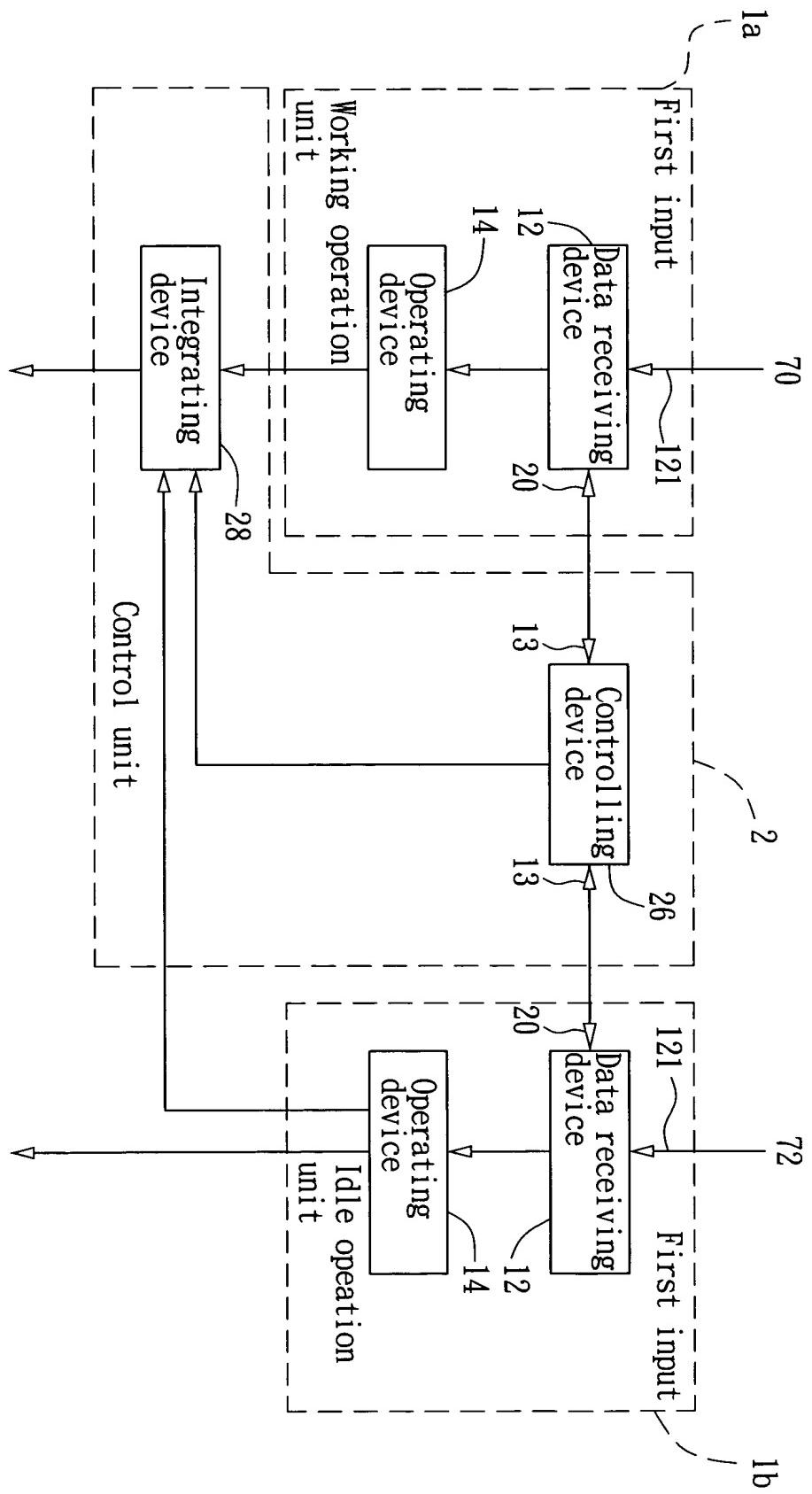


FIG. 4